The libRoadRunner SBML JIT Compiler and Simulation Library

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• A Plymouth RoadRunner?



• A Plymouth RoadRunner?



No, we can actually take a corner!

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• A modular library for SBML JIT compilation, simulation and analysis.

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Features

- JIT Compiler
- Analysis Features (MCA, Steady State,...)
- Cross Platform: OSX (10.6 and up), Linux (RHEL 5 and up), Win32.
- \bullet C++ with extensive native Python binding

History

- Original C# library written by Herbert Sauro and Frank Bergmann.
- Line by line transliteration to C++ by Totte Karlsson
- Current version: Ground up new design
- Kept name, re-used libStruct and other analysis functionality in original RoadRunner
- Python API wraps C++ library with Pythonic sugar.
- C-API

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Modular Design

- Component based design, everything is pure virtual interfaces.
- Strict separation of model state and propagator.

$$\Gamma(t) = e^{i\mathcal{L}t}\Gamma(0)$$

- Dynamically pluggable models (system state) and integrators (propagator)
 - LLVM JIT is primary,
 - LLVM MCJIT prototype for ARM (developed by Kyle Medley)
 - GPU based on OpenCL (Kyle Medley)
 - CVODE based integrator is primary
 - Gillespie Direct Method integrator.
 - RK45 integrator (or is it just RK4?)

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SBML as a Declarative Language

- Computation is specified via rules and reactions
- Fully compliant SBML event system
- Assignment rules
- Initial assignment rules
- Functions
 - most implentations treat them as macros
 - not always the most efficient form.
 - trade off between function call overhead and increase in code segment size.
 - macros increase JIT compilation time
 - developing heuristics of when more efficient to treat as macro or function.
 - function as macros yield dynamic scoping
 - most simulators expand functions inline, in order to be compatible, we implement dynamic scoping.

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SBML as a Declarative Language

Reactions

$$rac{d}{dt} \mathbf{S}(t) = \mathbf{N}(t) \cdot
u(\mathbf{S}(t), \mathbf{p})$$

- Rate Rules
- System Dynamics with Events

$$\mathbf{S}(t) = \sum_{E} \int_{t_i}^{t_i+1} \dot{\mathbf{S}}(\mathbf{p}_i, t) dt$$

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- Compiler design overview: (1) lexical (2) syntactic, (3) semantic (4) intermediate code generation, (5), code optimizer, and (6) native code generator.
- Phases 1 through 4 are the analysis phase.
 - Source code is separated into parts and then arranged into a meaningful structure (or grammar of the language).
- Stages 5 through 6 are the synthesis phases.
 - Executable machine code is generated.
- Initial and final stages are generalizable.
- First phases are handled by libSBML.
- Final phase is handled by LLVM.
- We perform the semantic, intermediate and partially code optimization phases.

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• libSBML effectively yields an AST

Infix	MathML	AST					
x + 2 + (y * 5)	$$						
	<apply></apply>						
	<plus></plus>	+					
	<ci>x</ci>						
	<cn>2<cn></cn></cn>						
	<apply></apply>						
	<times></times>	(\mathbf{x}) (2) (*)					
	<ci>y</ci>						
	<cn>5</cn>						
		y (5)					

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- We generate intermediate language representation.
- Processed by LLVM to generate machine native code
- LLVM is re-targetable, x86 is most common,
- Also supports ARM, SPARC, PPC, etc code generators
- GPU code generation in development.
- LLVM IR is SSA or Single Static Assignment:
 - each var may be assigned exactly once
 - contract to Java byte code or MSIL which are stack based ILs.
- Simple low level form suitable for further analysis, optimization and native machine code generation.
- Fairly easy to look at LLVM IR and immediately tell what x86 instructions will be generated.

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- We have very simple expression generator.
- Does produce large number of redundant operations.
- LLVM optimization pass performs constant folding, instruction combining, dead code elimination, etc...
- Mixed Mode Arithmetic

(+ 1.123 1.1 5), (* 1.0 2.34 (> A 5)).

- Results of AST subtree may be logical, integer or double.
- Sign extend logicals to double for arithmetic.
- Integers and doubles used in logical operations are checked for zero to yield logical (fcmp, icmp)

- Data layout is tuned specifically for each model.
- All state variables are grouped in a contiguous block.
- State variables are accessed directly by the integrator, (single pointer assignment, no copying is involved).
- Lazy Evaluation
 - No redundant state variables are ever created.
 - Only store amounts
 - Everything is accessed via JITed accessors functions.
 - Rules are compiled into accessors functions.
 - Accessor functions are effectively a indirect branch (jmp *%eax)
 - Address is computed at compile time (jump table).

Symbol Resolution

- Each symbol in procedural languages typically correspond to a single location.
- Symbol resolution is handled with a symbol table
 - Maps symbols to memory locations
 - May be chained local global scope.
- In SBML, symbols mean different things at different times
 - Initial assignment, assignment and functions apply at different times.
 - Symbols are not assigned memory locations if they are assignment rules.
- Sometimes a symbol table is not enough
- We created a symbol forest
- Replacement rules are resolved in the symbol forest.
- Can be chained
- Function to model to initial state scope.

Performance

Benchmarking: Brusselator and Piecewise Functions

			std	stiff					std	stiff	
	elat	ors Run	IPT INTE	ST .	T Sim		Bul	Runne	RUINE	л Л	T.Sim
#B	ilibR	oadu libRo	adRunne COP	ASI LIDSBI	SBSCL	# P	tate Bul	adu libRos	adi COP	st - stiff ISI libSBI	SBSCL
50	0.5	0.8	0.9	12.2	13.0	1	1.60	1.84	12.3	N/A	2.4
100	0.9	2.3	3.8	50.5	46.1	2	2.14	2.61	24.1	N/A	13:20
150	1.1	4.4	7.2	N/A	1:52	3	2.71	3.68	35.2	N/A	39:52
200	1.9	7.2	13.2	N/A	3:51	4	3.39	4.83	46.5	N/A	1:32:32
250	2.6	11.1	21.8	N/A	8:37	5	4.20	6.48	58.4	N/A	3:04:21
300	3.3	15.6	30.1	N/A	14:09						
350	3.9	21.5	46.3	N/A	21:11						
400	4.7	28.6	55.7	N/A	33:35						
450	5.6	36.3	1:14	N/A	48:12						
500	6.6	44.5	1:35	N/A	1:14:21						

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Performance

Benchmarking: Standard vs. Stiff Solver

model	9	14	22	33	repressilator		
simulation time	150	300	2000	60	10e4		
# state variables	22	86	28	10	6		
absolute	1e-15	1.0e-4	1.0e-4	1.0e-4	1.0e-4		
relative	1e-9	1.0e-9	1.0e-9	1.0e-9	1.0e-9		
dynamics	T / S	Т/ S	Т/ S	oscil	oscil / stiff		
libRoadRunner - stiff	95	510	230	125	1,040		
libRoadRunner - std	760	920	235	180	3,320		
COPASI	200	1,980	510	250	1,600		
SBSCL	1,700	6,950	25,300	2,200	98,000		
2005 SOSLib Data *Not comparable to above data							
absolute	1.0e-4	1.0e-4	1.0e-4	1.0e-4	1.0e-14		
relative	1.0e-9	1.0e-9	1.0e-9	1.0e-9	1.0e-9		
Dizzy 1.11.1 ODEtoJava-dopr54-adaptive	15,499	12,711	$2,\!634$	$19,\!350$	6,369		
Jarnac 2.16n	344	14,531	1,157	5,843	4,516		
SBMLToolbox	188	920	302	5,554	6,681		
MatlabR14SP3ode15s COPASI 4.0 Build 15	156	4,062	109	1,437	500		
SOSlib 1.6.0pre from CVS, Nov. 17th 2005	234	515	171	562	1,062		

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Sparse Multiply

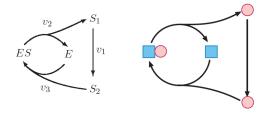
- Linear vs Quadratic scaling.
- Faster to perform CSR than JIT multiply
- Need to consider size of L1 and L2 caches
- CSR code segment fits entirely in L1 cache
- JITed code requires continuous fetch for code and data segments.
- Is a bad choice when stoichiometric coefficients are state variables.
- Rarely encountered optimize for most common situations.

Integrators

- New integrators only require implementing the Integrator interface and registering with the IntegratorFactory.
- We plan on investigating many new integrators.
- LSODA not very suitable: FORTRAN with global state vars can not be used in parallel.
- GPU based integrator currently being developed by Kyle Medley.
- New multi-scale integrators Sundials ARKode suite will be implemented.

Conserved Moieties

• What are they and why should you care.



 $\mathbf{S}_d(t) = \mathbf{L}_0 \mathbf{S}_i(t) + \mathbf{T}.$

- Conserved Moiety Converter
- libSBML extension and converter.
- Mutable Conserved Moieties and JIT

Native Python API

- Designed from the ground up as native Python module
- SWIG'ed C++ with heavy customization
- Used 100% native Python types lists, numpy array, etc...
- Completely self contained
- Designed to feel like part of SciPy

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Native Python API

- Designed to be embedded in existing simulators
- Full access to everything via numpy arrays
- No copying

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Python API Interactive Use Features

- Dynamic Python properties.
- Native Python documentation.
- All variables accessible dictionaries.
- Selection syntax.

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Python API

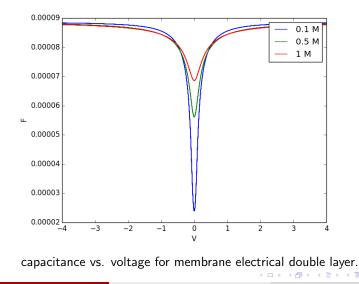
Event Hooks

- Tie into SBML event system
- Hook models into existing simulations.
- Register user callbacks.
- Any code in callbacks can modify any model parametere.
- def onEvent(integrator, model, eventId, time):
 print("onEvent, time: {}".format(time))
 model.SomeParameter = getSomeData(eventId)
- r = RoadRunner('test.xml')

```
listener = PyIntegratorListener()
listener.setOnEvent(onEvent)
r.getIntegrator().setListener(listener)
res = r.simulate(0, 10, 100)
```

Python API

Real Work: Membrane Protein Mediated RedOx Reactions



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Real Work: Membrane Protein Mediated RedOx Reactions

```
r = RoadRunner('cap.xml') # create a RoadRunner obj
r.M = 0.1 # set the molarity
def c(v): # cap as fuction of volt
r.V = v # set voltage
r.steadyState() # perform steady state
return r.C # return capacitance
```

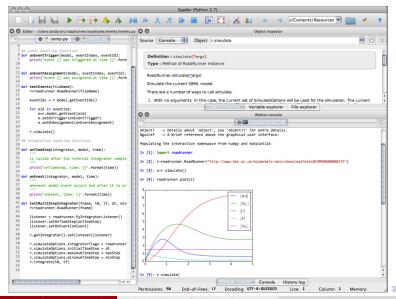
cap = [c(v) for v in arange(-4, 4, 0.1)]

Current Applications

- University of Washington: Tellurium
- Indiana University: CompuCell3D
- University of Southern California: Bouteiller Lab
- Charité Universitätsmedizin Berlin

Python API

Shameless Tellurium Plug



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Definitly Not Plymouth RoadRunner.



Small, Light and Fast: More like a Lotus Elise.

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